

From: bogus@keysight.com
Sent: 19 Dec 2022 22:32:42 -0600
Attachments: ATT00001.bin, ATT00002.bin, ATT00003.bin



Test Report

Overall Result: PASS

Test Configuration Details	
Application	
Name	D9010CPHC MIPI C-PHY Test
Version	1.70.4.0
Device Description	
Data Type	HS Signal
LP Escape Mode	No
T3 Prog Seq Mode	No
Calibration Preamble	No
Calibration Preamble Format	Format 1
HS Symbol Rate (Msps)	4520
CTS Version	v2.0
T3-PROGSEQ Sequence	01234012340123
HS Mode	Normal HS
LP Mode	Normal LP
Test Session Details	
Infiniium SW Version	06.72.00501
Infiniium Model Number	DSAV134A
Infiniium Serial Number	MY57500104
Debug Mode Used	No
Compliance Limits	MIPI C-PHY Test Limit v2.0 (official)
Last Test Date	2022-12-19 16:20:16 UTC -06:00

Summary of Results

Test Statistics	
Failed	0
Passed	1
Total	1

Margin Thresholds	
Warning	< 5 %
Critical	< 0 %

Pass	# Failed	# Trials	Test Name	Actual Value	Margin	Pass Limits
✓	0	1	1.2.21 HS-TX Eye Diagram (VABC)[CTLE][above 3.5Gsps](C)	Pass	100.0 %	Pass/Fail

Report Detail

[Next](#)

✓ 1.2.21 HS-TX Eye Diagram (VABC)[CTLE][above 3.5Gps](C) Reference:
Pass Test Description: Transmitter eye diagram test with CTLE for

VABC

Pass Limits:Pass/Fail3107_Eye Diagram (VABC)[CTLE][above 3.5Gps]Pass

Result Details:

Result Details

Number of Mask Violation0.0**CTLE Index**CTLE5**DC Gain**1.000**Zero Frequency**1.000

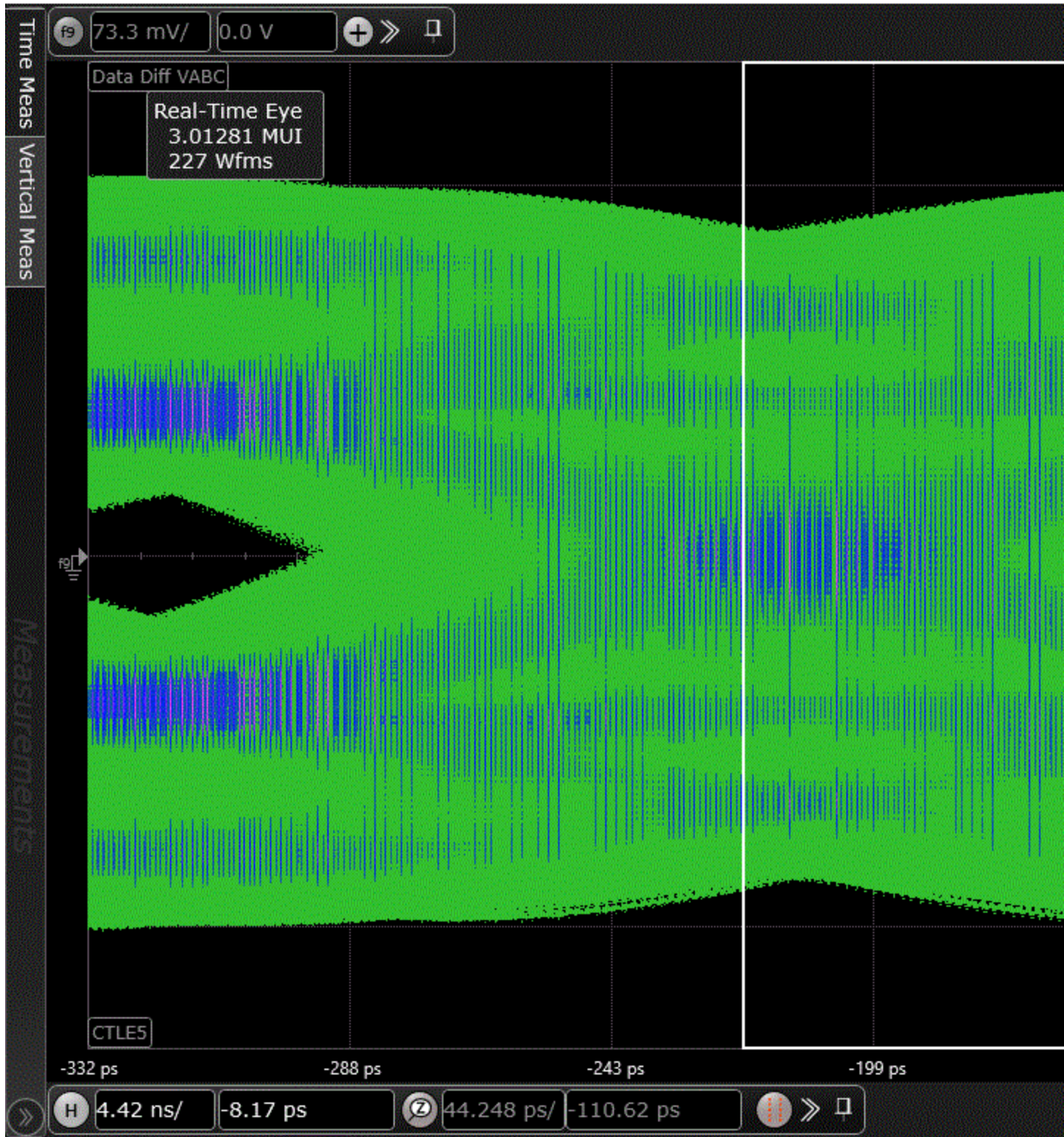
GHz**Pole 1 Frequency**2.800 GHz**Pole 2 Frequency**10.000 GHz**Vterm(V)**Not

Applicable**Mask Type**Specification**Mask**Prorated(1E-6)**Eye Diagram (VABC) Screen**

Capture(See image)**Reference Channel**Standard Reference Channel + Package
Model

Trial 1

Trial 1: Eye Diagram (VABC) Screen Capture



[Top Previous](#)

