



## **Arasan Chip Systems is look for Senior Analog Engineers for development of High Speed SERDES PHYs**

CMOS design engineer with strong analog design expertise as well as mixed-signal design capabilities.

### **Job Description:**

- 6- 10+ years design experience in CMOS Analog / Mixed Signal Circuit Design using advance digital process technology.
- Experience in at least one of these high speed interfaces USB2.0, USB3.0, D-PHY, M-PHYI, M-PHYII, USHII, PCI express, SATA etc
- Strong fundamental understanding of transistor devices and analog transistor level design
- Hands on design experienced in high speed low jitter PLL, DLL , transmitters, receivers, equalization, oscillator, biasing circuits, bandgap references, regulators, op-amps and high speed clocking circuits.
- Experience in usage of IC design tools like Cadence / Mentor for Analog Circuit Design, Circuit Simulation, System Simulation, and Layout design, Physical Design, Physical Verification, Parasitic Extraction and Full Chip Verification.
- Scripting Skills (Perl) as well as modeling and design skills in Verilog/Verilog AMS
- Will be required to work independently with limited supervision and be a team player

### **Job Responsibilities:**

- Design and Implementation of analog modules required for High Speed SERDES design.
- Design, simulate, and verify analog and mixed-signal CMOS circuits.
- Supervise closely IC layout/mask designers, provide floorplan and layout guidelines.
- Mentor Junior Team members to learn new design skills
- Debug and characterization of silicon design in lab.
- Should posses a strong ability to learn and explore new technologies and demonstrate good analysis and problem solving skills.

### **Educational Qualifications:**

- BS, MS or PhD in Electrical Engineering Preferred.

**About Arasan**

**Enabling Technology Adoption through Total IP Solutions - The Bus Stops Here!**

Arasan Chip Systems based in San Jose, CA, USA, is a world-leading supplier of SoC Intellectual Property Solutions with a successful 15 year track record. Arasan delivers technology-leading IP focused on Bus Interfaces such as MIPI, SDIO, USB, PCIe, Ethernet, and Flash Storage Solutions of NAND, eMMC, SD, CF+, UFS and more, to the global electronics market. Arasan's Total Solution Approach starts with Technology Consulting using our Domain Expertise and includes all the building blocks required for technology adoption- RTL IP cores, Analog Mixed Signal IP Cores, Verification IP, Portable Software Drivers / Stacks, Protocol Test & Analyzers, HDK's, and associated design services. Arasan Total IP Solution enables the rapid adoption of emerging technologies reducing our customer's time to market while minimizing risk and ensuring compliance to respective standards.

For further information please contact:

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